

L Number	Hits	Search Text	DB	Time stamp
-	21350	switch same instructions and @ad<20000731	USPAT; US-PGPUB	2003/09/12 11:27
-	1426	(switch same instructions and @ad<20000731) and "count value"	USPAT; US-PGPUB	2003/09/10 15:03
-	31	((switch same instructions and @ad<20000731) and "count value") and redirection	USPAT; US-PGPUB	2003/09/10 15:03
-	7	("line card" same fail\$3) and @ad<20000730) and redirection	USPAT; US-PGPUB	2003/09/11 08:14
-	174	((router and (line adj card\$)) and instructions) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 09:00
-	7	reprogramming and "switch instructions" and @ad<20000730	USPAT; US-PGPUB	2003/09/11 09:34
-	1	("synchronous switch" same instruction\$2) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 09:05
-	38	("synchronous switch" and instruction\$2) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 09:11
-	1	("switch core" and @ad<20000730) and ("instruction system")	USPAT; US-PGPUB	2003/09/11 09:11
-	430	"switch core" and @ad<20000730	USPAT; US-PGPUB	2003/09/11 09:13
-	5	("switch core" and @ad<20000730) and "instruction memory"	USPAT; US-PGPUB	2003/09/11 09:17
-	417	"IAD" and @ad<20000730	USPAT; US-PGPUB	2003/09/11 09:17
-	389	"time slot interchanger" and @ad<20000730	USPAT; US-PGPUB	2003/09/11 09:18
-	88	("time slot interchanger" and @ad<20000730) and ("instruction system" or "programming")	USPAT; US-PGPUB	2003/09/11 09:27
-	0	"synchronous switch" and "instruction system" and @ad<20000730	USPAT; US-PGPUB	2003/09/11 09:28
-	129427	(switch and counter) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 09:29
-	136	programming and (switch adj instruction\$) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 09:48
-	13	"integrated access device" and @ad<20000730	USPAT; US-PGPUB	2003/09/11 09:48
-	154	("line card" same fail\$3) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 09:54
-	42	((line adj card\$) near5 fail\$3) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 10:05
-	284	switch same (instruction adj memory) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 10:10
-	2	(line adj card\$) same "instruction set" and @ad<20000730	USPAT; US-PGPUB	2003/09/11 10:11
-	18	(line adj card\$) near5 instruction\$ and @ad<20000730	USPAT; US-PGPUB	2003/09/11 10:18
-	1	switch and (redirection adj memory) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 10:20
-	0	IAD and (redirection adj memory) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 10:20
-	0	"IAD" and (redirection adj memory) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 10:21
-	0	router and (redirection adj memory) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 10:21
-	0	router and (redirection adj memory) and @ad<20000730	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 10:21
-	1	switch and (redirection adj memory) and @ad<20000730	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 10:24
-	4673	(switch and fault and recovery) and @ad<20000730	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 10:25

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-	2169	((switch and fault and recovery) and @ad<20000730) and counter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 10:25
-	1326	((((switch and fault and recovery) and @ad<20000730) and counter) and instructions and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 10:26
-	250	((((switch and fault and recovery) and @ad<20000730) and counter) and instructions and memory) and "MSB"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 10:26
-	196	(((((switch and fault and recovery) and @ad<20000730) and counter) and instructions and memory) and "MSB") and LSB	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 10:47
-	0	709.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 10:47
-	0	709/*.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 10:48
-	0	(709/*).CCLS.	USPAT; US-PGPUB	2003/09/11 10:48
-	28022	("709").CLAS.	USPAT; US-PGPUB	2003/09/11 10:48
-	38	((("709").CLAS.) and "IAD"	USPAT; US-PGPUB	2003/09/11 11:01
-	48229	instructions and switch and @ad<20000730	USPAT; US-PGPUB	2003/09/11 11:03
-	5052	("instruction set" and switch) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 11:04
-	124	((("instruction set" and switch) and @ad<20000730) and ("redirection memory" or "re-direction memory" or redirection or "re-direction")	USPAT; US-PGPUB	2003/09/11 11:20
-	490	router near4 memory and @ad<20000730	USPAT; US-PGPUB	2003/09/11 11:21
-	50	router adj memory and @ad<20000730	USPAT; US-PGPUB	2003/09/11 11:32
-	173	switch same (instruction adj memory) and @ad<20000730	USPAT; US-PGPUB	2003/09/11 12:45
-	4520	programming and switch and recovery and @ad<20000730	USPAT; US-PGPUB	2003/09/11 12:50
-	75	(programming and switch and recovery and @ad<20000730) and "instruction memory"	USPAT; US-PGPUB	2003/09/11 12:51
-	75	(programming and switch and recovery and @ad<20000730) and "instruction memory"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 12:58
-	626	switch same "routing table" and @ad<20000730	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 13:03
-	1	6256293.pn.	USPAT; US-PGPUB	2003/09/12 11:27
-	81	(line near card) same failure and @ad<20000731	USPAT; US-PGPUB	2003/09/12 11:36
-	0	((line near card) same failure and @ad<20000731) and redirection	USPAT; US-PGPUB	2003/09/12 11:28
-	24	((line near card) same failure and @ad<20000731) and programming	USPAT; US-PGPUB	2003/09/12 11:28

-	2	recover\$3 and "line card failure" and @ad<20000731	USPAT;	2003/09/12 11:41
-	559	("line card" and failure) and @ad<20000731	US-PGPUB	2003/09/12 11:51
-	6	("5570362" or "5577039" or "5734653" or "6243382" or "6404782" or "6442167").pn.	USPAT;	2003/09/12 13:19
-	2	(redirection adj memory) and @ad<20000731	US-PGPUB	2003/09/12 13:21
-	0	(re-direction adj memory) and @ad<20000731	USPAT;	2003/09/12 13:21
-	45	(redirect adj memory) and @ad<20000731 NOT ((redirection adj memory) and @ad<20000731)	US-PGPUB	2003/09/12 13:27
-	4	"customer line card\$" and @ad<20000731	USPAT;	2003/09/12 13:29
-	2107	"line card\$" and @ad<20000731	US-PGPUB	2003/09/12 13:30
-	104	("line card\$" and @ad<20000731) and (reprogram\$5 or re-program\$5)	USPAT;	2003/09/12 13:32
-	0	((("line card\$" and @ad<20000731) and (reprogram\$5 or re-program\$5)) and memory) and "synchronous switch"	US-PGPUB	2003/09/12 13:33
-	99	((("line card\$" and @ad<20000731) and (reprogram\$5 or re-program\$5)) and memory	USPAT;	2003/09/12 13:44
-	440	370/216.ccls.	US-PGPUB	2003/09/12 13:59
-	0	370/216.ccls. and "synchronous switch"	USPAT;	2003/09/12 14:00
-	358	"synchronous switch" and @ad<20000731	US-PGPUB	2003/09/12 14:00
-	0	("synchronous switch" and @ad<20000731) and "faulty recovery"	USPAT;	2003/09/12 14:00
-	98	fault same ("linecard" or "line card") and @ad<20000731	US-PGPUB	2003/09/12 14:02
-	42	(fault same ("linecard" or "line card") and @ad<20000731) and recover\$4	USPAT;	2003/09/12 14:03
-	13	"re-mapping" near3 instructions and @ad<20000731	US-PGPUB	2003/09/12 14:03
-	34	"routing parameter\$" and @ad<20000731	USPAT;	2003/09/12 14:24
-	0	("synchronous switch" and @ad<20000731) and "routing memory"	US-PGPUB	2003/09/12 14:26
-	95	("synchronous switch" and @ad<20000731) and memory	USPAT;	2003/09/12 14:28
-	24	((("synchronous switch" and @ad<20000731) and memory) and instruction\$5	US-PGPUB	2003/09/15 08:06
-	1868	(rout\$4 near1 memory) and @ad<20000731	USPAT;	2003/09/15 08:09
-	17	((rout\$4 near1 memory) and @ad<20000731) and ("linecard" or (line near1 card))	US-PGPUB	2003/09/15 08:20
-	3	"routing parameter set" and @ad<20000731	USPAT;	2003/09/15 08:24
-	8	reprogramming same (rout\$4 adj table) and @ad<20000731	US-PGPUB	2003/09/15 08:25
-	137	(switch adj recover\$4) and @ad<20000731	USPAT;	2003/09/15 08:32
-	0	((switch adj recover\$4) and @ad<20000731) and ("line card" or "linecard")	US-PGPUB	2003/09/15 08:32
-	55	((switch adj recover\$4) and @ad<20000731) and memory	USPAT;	2003/09/15 08:40
-	11264	switch and (memory near10 map\$4) and @ad<20000731	US-PGPUB	2003/09/15 08:40
-	10268	switch and (memory near5 map\$4) and @ad<20000731	USPAT;	2003/09/15 08:40
-	1111	switch same (memory near5 map\$4) and @ad<20000731	US-PGPUB	2003/09/15 08:40
-	27	(switch same (memory near5 map\$4) and @ad<20000731) and ("linecard" or "line card")	USPAT;	2003/09/15 08:41
-	18	programmable near5 (rout\$4 adj table) and @ad<20000731	US-PGPUB	2003/09/15 10:09
-	2724	(rout\$4 adj table) and @ad<20000731	USPAT;	2003/09/15 09:44
-			US-PGPUB	

-	1229	((rout\$4 adj table) and @ad<20000731) and instruction\$	USPAT; US-PGPUB	2003/09/15 09:44
-	1022	((rout\$4 adj table) and @ad<20000731) and instruction\$) and memory	USPAT; US-PGPUB	2003/09/15 09:44
-	27	((((rout\$4 adj table) and @ad<20000731) and instruction\$) and memory) and ("line card" or "linecard")	USPAT; US-PGPUB	2003/09/15 09:47
-	742	instruction\$2 and ("line card\$" or "linecard\$") and @ad<20000731	USPAT; US-PGPUB	2003/09/15 09:48
-	385	(instruction\$2 and ("line card\$" or "linecard\$") and @ad<20000731) and routing	USPAT; US-PGPUB	2003/09/15 09:48
-	325	((instruction\$2 and ("line card\$" or "linecard\$") and @ad<20000731) and routing) and table	USPAT; US-PGPUB	2003/09/15 09:48
-	209	((((instruction\$2 and ("line card\$" or "linecard\$") and @ad<20000731) and routing) and table) and failure	USPAT; US-PGPUB	2003/09/15 09:57
-	0	reprogrammable near5 (rout\$4 adj table) and @ad<20000731	USPAT; US-PGPUB	2003/09/15 09:57
-	0	re-programmable near5 (rout\$4 adj table) and @ad<20000731	USPAT; US-PGPUB	2003/09/15 09:57
-	2	(redirection adj memory) and @ad<20000731	USPAT; US-PGPUB	2003/09/15 10:07
-	123	program\$4 near5 (rout\$4 adj table) and @ad<20000731	USPAT; US-PGPUB	2003/09/15 10:09
-	1	((program\$4 near5 (rout\$4 adj table) and @ad<20000731) and instruction\$2) and ("line card\$" or "linecard\$")	USPAT; US-PGPUB	2003/09/15 10:10
-	75	(program\$4 near5 (rout\$4 adj table) and @ad<20000731) and instruction\$2	USPAT; US-PGPUB	2003/09/15 10:17
-	0	("line card\$" or "linecard\$") and "routing parameter\$" and @ad<20000731	USPAT; US-PGPUB	2003/09/15 10:22
-	356	("line card\$" or "linecard\$") and "parameter\$" and @ad<20000731	USPAT; US-PGPUB	2003/09/15 10:19
-	296	((("line card\$" or "linecard\$") and "parameter\$" and @ad<20000731) and rout\$4	USPAT; US-PGPUB	2003/09/15 10:20
-	0	("line card\$" or "linecard\$") same "instruction\$2" and @ad<20000731	USPAT; US-PGPUB	2003/09/15 10:24
-	743	(line adj card) and fail\$4 and @ad<20000731	USPAT; US-PGPUB	2003/09/15 10:25
-	284	((line adj card) and fail\$4 and @ad<20000731) and (memory and instruction\$1)	USPAT; US-PGPUB	2003/09/15 10:25
-	71	(line adj card) WITH fail\$4 and @ad<20000731	USPAT; US-PGPUB	2003/09/15 10:33
-	38783	(controller or processor) WITH (updat\$3 or modif\$4 or program\$2) same ((rout\$3 adj table) or memory) and @ad<20000731	USPAT; US-PGPUB	2003/09/15 10:35
-	29967	(controller or processor) WITH (updat\$3 or modif\$4 or program\$2) WITH ((rout\$3 adj table) or memory) and @ad<20000731	USPAT; US-PGPUB	2003/09/15 10:35
-	29967	((controller or processor) WITH (updat\$3 or modif\$4 or program\$2) WITH ((rout\$3 adj table) or memory)) and @ad<20000731	USPAT; US-PGPUB	2003/09/15 10:35
-	87	((controller or processor) WITH (updat\$3 or modif\$4 or program\$2) WITH ((rout\$3 adj table))) and @ad<20000731	USPAT; US-PGPUB	2003/09/15 10:38
-	49	((controller or processor) WITH (updat\$3 or modif\$4 or program\$2) WITH (rout\$3 adj table) and instruction\$2) and @ad<20000731	USPAT; US-PGPUB	2003/09/15 10:42
-	574	((controller or processor) WITH (updat\$3 or modif\$4 or program\$2) WITH (instruction\$2 adj memory)) and @ad<20000731	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/15 10:46
-	266	((((controller or processor) WITH (updat\$3 or modif\$4 or program\$2) WITH (instruction\$2 adj memory)) and @ad<20000731) and switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/15 10:47

-	13	(((((controller or processor) WITH (updat\$3 or modif\$4 or program\$2) WITH (instruction\$2 adj memory)) and @ad<20000731) and switch) and telecommunication\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/15 10:53
-	80	"line card" same recover\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/15 10:57
-	42	("line card" same recover\$2) and @ad<20000731	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/15 10:53
-	91	("line card" WITH fail\$3) and @ad<20000731	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/15 11:19
-	13	((("line card" WITH fail\$3) and @ad<20000731) and (reprogram\$4 or remap\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/15 11:09
-	0	("line card" with "instruction\$2 memory") and @ad<20000731	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/15 11:20
-	0	("line card" same "instruction\$2 memory") and @ad<20000731	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/15 11:20
-	0	("line card" and "instruction\$2 memory") and @ad<20000731	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/15 11:20
-	0	(instructions with "line card\$1") and @ad<20000731	USPAT; US-PGPUB	2003/09/15 11:24
-	0	(parameters with "line card\$1") and @ad<20000731	USPAT; US-PGPUB	2003/09/15 11:24
-	0	(table with "line card\$1") and @ad<20000731	USPAT; US-PGPUB	2003/09/15 11:24
-	0	(table same "line card\$1") and @ad<20000731	USPAT; US-PGPUB	2003/09/15 11:24
-	0	(table and "line card\$1") and @ad<20000731	USPAT; US-PGPUB	2003/09/15 11:24
-	785	(table and "line card") and @ad<20000731	USPAT; US-PGPUB	2003/09/15 11:24
-	20	(instructions with "line card") and @ad<20000731	USPAT; US-PGPUB	2003/09/15 11:30
-	280	370/217.ccls.	USPAT; US-PGPUB	2003/09/15 11:59
-	241	370/217.ccls. and @ad<20000731	USPAT; US-PGPUB	2003/09/15 13:09
-	138	370/218.ccls. and @ad<20000731	USPAT; US-PGPUB	2003/09/15 13:08
-	67	370/219.ccls. and @ad<20000731	USPAT; US-PGPUB	2003/09/15 13:08
-	9	(370/217.ccls. and @ad<20000731) and ("linecard" or "line card")	USPAT; US-PGPUB	2003/09/15 13:09
-	9	("5144622" "5416771" "5479401" "5689512" "5694463" "5781529" "5872783" "6101188" "6151297").PN.	USPAT	2003/09/15 13:28

-	18	("4274138" "4338663" "4839797" "4876639" "5193158" "5327566" "5392408" "5404472" "5420992" "5475824" "5481684" "5481693" "5524211" "5568646" "5574928" "5598546" "5638525" "5758115").PN.	USPAT	2003/09/15 13:29
-	3	("4884263" "5590118" "5764624").PN.	USPAT	2003/09/15 13:31
-	0	("line card" or "linecard") WITH "fault recovery" and @ad<20000731	USPAT; US-PGPUB	2003/09/15 13:34
-	2	("line card" or "linecard") same "fault recovery" and @ad<20000731	USPAT; US-PGPUB	2003/09/15 13:52
-	7	reprogram\$4 same (rout\$2 adj table) and @ad<20000731	USPAT; US-PGPUB	2003/09/15 13:53
-	21	reprogram\$4 and switch and (rout\$2 adj table) and @ad<20000731	USPAT; US-PGPUB	2003/09/15 13:54
-	1	370/216.ccls. and "instruction register" and @ad<20000731	USPAT; US-PGPUB	2003/09/15 13:55
-	0	370/217.ccls. and "instruction register" and @ad<20000731	USPAT; US-PGPUB	2003/09/15 13:55
-	0	370/218.ccls. and "instruction register" and @ad<20000731	USPAT; US-PGPUB	2003/09/15 13:55
-	0	370/219.ccls. and "instruction register" and @ad<20000731	USPAT; US-PGPUB	2003/09/15 13:55
-	5900	(switch or router) near5 instruction\$2 and @ad<20000731	USPAT; US-PGPUB	2003/09/16 10:05
-	817	((switch or router) near5 instruction\$2 and @ad<20000731) and "instruction set"	USPAT; US-PGPUB	2003/09/16 10:06
-	0	((((switch or router) near5 instruction\$2 and @ad<20000731) and "instruction set") and "instruction memory") and "routing parameters"	USPAT; US-PGPUB	2003/09/16 10:06
-	234	((((switch or router) near5 instruction\$2 and @ad<20000731) and "instruction set") and "instruction memory"	USPAT; US-PGPUB	2003/09/16 10:24
-	6	((((switch or router) near5 instruction\$2 and @ad<20000731) and "instruction set") and ("linecard" or "line card")	USPAT; US-PGPUB	2003/09/16 10:07
-	6402	(rout\$5 near3 table) and @ad<20000731	USPAT; US-PGPUB	2003/09/16 10:27
-	497	((rout\$5 near3 table) and @ad<20000731) and (instruction adj set)	USPAT; US-PGPUB	2003/09/16 10:25
-	0	((((rout\$5 near3 table) and @ad<20000731) and (instruction adj set)) and (instruction adj memory)) and (rout\$4 adj parameter\$)	USPAT; US-PGPUB	2003/09/16 10:25
-	0	((rout\$5 near3 table) and @ad<20000731) and ((((rout\$5 near3 table) and @ad<20000731) and (instruction adj set)) and (instruction adj memory)) and (rout\$4 adj parameter\$))	USPAT; US-PGPUB	2003/09/16 10:26
-	65	((rout\$5 near3 table) and @ad<20000731) and (instruction adj set)) and (instruction adj memory)	USPAT; US-PGPUB	2003/09/16 10:26
-	2733	(rout\$5 adj table) and @ad<20000731	USPAT; US-PGPUB	2003/09/16 10:35
-	15	((((rout\$5 adj table) and @ad<20000731) and (((rout\$5 near3 table) and @ad<20000731) and (instruction adj set))) and (((rout\$5 near3 table) and @ad<20000731) and (instruction adj set)) and (instruction adj memory))	USPAT; US-PGPUB	2003/09/16 10:27
-	139	((rout\$5 adj table) and @ad<20000731) and (((rout\$5 near3 table) and @ad<20000731) and (instruction adj set))	USPAT; US-PGPUB	2003/09/16 10:30
-	2073	(routing adj table) and @ad<20000731	USPAT; US-PGPUB	2003/09/16 10:39
-	3	((((routing adj table) and @ad<20000731) and (((rout\$5 near3 table) and @ad<20000731) and (instruction adj set))) and (((rout\$5 near3 table) and @ad<20000731) and (instruction adj set)) and (instruction adj memory))	USPAT; US-PGPUB	2003/09/16 10:36
-	74	((routing adj table) and @ad<20000731) and (((rout\$5 near3 table) and @ad<20000731) and (instruction adj set))	USPAT; US-PGPUB	2003/09/16 10:36

-	19	((instruction adj table) and @ad<20000731) and (((rout\$5 near3 table) and @ad<20000731) and (instruction adj set))	USPAT; US-PGPUB	2003/09/16 10:40
-	11	((instruction adj table) and @ad<20000731) and (((rout\$5 near3 table) and @ad<20000731) and (instruction adj set)) and (instruction adj memory))	USPAT; US-PGPUB	2003/09/16 10:40
-	30	(instruction adj table) same (switch or router) and @ad<20000731	USPAT; US-PGPUB	2003/09/16 10:47
-	492	(instruction adj table) and @ad<20000731	USPAT; US-PGPUB	2003/09/16 11:00
-	78	("linecard" or "line card") same instruction\$ and @ad<20000731	USPAT; US-PGPUB	2003/09/16 11:01
-	306	synchronous adj (switch) and @ad<20000731	USPAT; US-PGPUB	2003/09/16 13:25
-	306	synchronous adj switch and @ad<20000731	USPAT; US-PGPUB	2003/09/16 13:26
-	0	(5848067,5943,339).pn.	USPAT; US-PGPUB	2003/09/16 13:27
-	2	("5848067" or 5943,339).pn.	USPAT; US-PGPUB	2003/09/16 13:27